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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/821,156	04/08/2004	Ko-Pin Chang	24061.83	9194
42717	7590	12/07/2005	(TSMC2003-0893)	
HAYNES AND BOONE, LLP 901 MAIN STREET, SUITE 3100 DALLAS, TX 75202			EXAMINER VON BUHR, MARIA N	
			ART UNIT 2125	PAPER NUMBER

DATE MAILED: 12/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/821,156

Applicant(s)

CHANG ET AL.

Examiner

Maria N. Von Buhr

Art Unit

2125

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 21 October 2005.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-22 and 25-29 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-22 and 25-29 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 April 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
- Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
- Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

1. A request for continued examination under 37 CFR §1.114, including the fee set forth in 37 CFR §1.17(e), was filed in this application after final rejection. Since this application is eligible for continued examination under 37 CFR §1.114, and the fee set forth in 37 CFR §1.17(e) has been timely paid, the finality of the previous Office action has been withdrawn pursuant to 37 CFR §1.114. Applicant's submission filed on 21 October 2005 has been entered.
2. Claims 1-22 and 25-29 remain pending in this application.
3. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
4. Examiner notes that Applicant has provided no arguments/remarks concerning any of the rejections presented in the previous Office action. Accordingly, all the previously presented rejections will be repeated below, with changes reflecting only the instantly presented amendment.
5. The 35 U.S.C. §102(e) rejection of instantly amended claims 1, 6-8 and 10, as being clearly anticipated by Chen et al. (U.S. Patent No. 6,821,891), is deemed to have been overcome and is, therefore, withdrawn, similarly as presented in the previous Office action (paragraph 4).
6. The 35 U.S.C. §103(a) rejections of instantly amended claims 1, 2, 6-8 and 10, as being unpatentable over Iwata et al. (U.S. Patent No. 6,297,114) in view of either Huang et al. (U.S. Patent No. 6,597,964) or Wehrung et al. (U.S. Patent Application Publication No. 2002/0164242), are deemed to have been overcome and are, therefore, withdrawn, similarly as presented in the previous Office action (paragraph 5).
7. The 35 U.S.C. §103(a) rejections of instantly amended claims 3-5, as being unpatentable over Chen et al. (U.S. Patent No. 6,821,891) in view of Pasadyn et al. (U.S. Patent No. 6,678,570), and as being unpatentable over Iwata et al. (U.S. Patent No. 6,297,114) in view of either Huang et al. (U.S. Patent No. 6,597,964) or Wehrung et al. (U.S. Patent Application Publication No. 2002/0164242), further in view of Pasadyn et al. (U.S. Patent No. 6,678,570), are deemed to have been overcome and are, therefore, withdrawn, similarly as presented in the previous Office action (paragraph 7).
8. As presented in the previous Office action, claims 1, 6, 9-11, 16, 19, 21 and 29 were rejected under 35 U.S.C. §102(b) as being clearly anticipated by Hanak (U.S. Patent No. 4,593,644), which discloses a "continuous, in-line deposition system ... for coating large substrates. The apparatus includes load-lock

chambers for loading and unloading substrates arranged in carriers” (the abstract), wherein substrates on a carrier are loaded into processing chambers for deposition, through a cavity 40 and a subchamber 44 used as a buffer compartment (analogous to the instantly claimed “stocker”), in which gas purging is performed (see at least, Fig. 2, with associated text; col. 1, line 10 - col. 2, line 22; col. 7, lines 5-23; col. 9, lines 17-23 and 53-56). Applicant has provided no remarks concerning this rejection.

However, as per the new limitation “a plurality of process tools each in communication with the operating control system,” Hanak teaches a plurality of subchambers, each performing a different function, all under control of the operating control system (see, at least, col. 9, line 50 - col. 10, line 43). Further as per the new limitation “a process intermediate station ... that is not integral to any of the plurality of process tools,” Examiner notes that it would have been obvious to one having ordinary skill in the art at the time the invention was made to separate the stations/tools/chambers/subchambers, since it has been held that constructing a formerly integral structure in various elements involves only routine skill in the art. *Nerwin v. Erlichman*, 168 USPQ 177, 179. Accordingly, claims 1, 6, 9-11, 16, 19, 21 and 29 now stand rejected under 35 U.S.C. §103(a), as being unpatentable over Hanak (U.S. Patent No. 4,593,644).

9. Similarly as presented in the previous Office action, claims 2 and 18 stand rejected under 35 U.S.C. §103(a), as being unpatentable over Hanak (U.S. Patent No. 4,593,644), as applied to claims 1 and 16 above, further in view of Wehrung et al. (U.S. Patent Application Publication No. 2002/0164242).

Similarly as presented in previous Office actions, Wehrung et al. disclose a “control system for transferring and buffering material in a material transport system. A transport system and method for moving an article between a conveyor and a workstation. A robot works in conjunction with transportation buffer control system to move Pods between storage shelves, load ports and I/O ports without intervention of the material handling controller. The robots include vertical movement mechanisms and horizontal movement mechanisms together with gripping devices to handle the Pods. Movement of Pods between storage shelves, load ports and I/O ports is seen as a single activity by the material control system” (the abstract), including the well-known use of various transfer carriers and MCS in a semiconductor fabrication facility (see at least, paragraph 0017). It would have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such transfer carriers in the system of Hanak, because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416. Applicant has provided no remarks concerning this rejection.

10. Similarly as presented in the previous Office action, claims 3-5, 12-15, 17 and 25-28 stand rejected under 35 U.S.C. §103(a), as being unpatentable over Hanak (U.S. Patent No. 4,593,644), as applied to claims 1, 11 and 16 above, further in view of Pasadyn et al. (U.S. Patent No. 6,678,570).

Similarly as presented in previous Office actions, Pasadyn et al. disclose a “method for determining output characteristics of a workpiece includes generating a tool state trace related to the processing of a workpiece in a tool; comparing the generated tool state trace to a library of reference tool state traces, each reference tool state trace having an output characteristic metric; selecting a reference tool state trace closest to the generated tool state trace; and determining an output characteristic of the workpiece based on the output characteristic metric associated with the selected reference tool state trace. A manufacturing system includes a tool and a tool state monitor. The tool is adapted to process a workpiece. The tool state monitor is adapted to generate a tool state trace related to the processing of a workpiece in the tool, compare the generated tool state trace to a library of reference tool state traces, each reference tool state trace having an output characteristic metric, select a reference tool state trace closest to the generated tool state trace, and determine an output characteristic of the workpiece based on the output characteristic metric associated with the selected reference tool state trace” (the abstract), in a semiconductor fabrication facility, including the well-known use of MES to schedule processes using various identification elements (see at least, col. 1, line 28 - col. 2, line 25; col. 3, line 54 - col. 4, line 34). It would have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such an operating system in the system of Hanak, because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416. Applicant has provided no remarks concerning this rejection.

11. Similarly as presented in the previous Office action, claims 7, 8, 20 and 22 stand rejected under 35 U.S.C. §103(a), as being unpatentable over Hanak (U.S. Patent No. 4,593,644), as applied to claims 1 and 16 above, further in view of Chen et al. (U.S. Patent No. 6,821,891).

Similarly as presented in previous Office actions, Chen et al. does not specifically provide for either a stocker or OHB as one of the devices. However, in this regard, Chen et al. does specify that the “processing chamber 200 may be integrated into an integrated processing platform, such as an Endura™ platform also available from Applied Materials, Inc.” (col. 5, lines 11-14), details of which were incorporated by reference. Such a platform inherently utilizes transfer stations, such as stockers, buffers and FOUPs (see for example, Vepa et al. (U.S. Patent Application Publication No. 2001/0027082 and U.S. Patent No. 6,852,012), Jevtic et al. (U.S. Patent Nos. 6,519,498 and 6,224,638) and Venkatesh et al. (U.S. Patent No. 6,074,443); all previously cited). Therefore, Chen et al. inherently provides for the presence of such transfer stations in a semiconductor

manufacturing line. It would have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such elements in the system of Hanak, because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

12. Claims 1, 6, 9-11, 16, 19, 21 and 29 are rejected under 35 U.S.C. §102(b) as being clearly anticipated by Yamashita et al. (U.S. Patent No. 5,746,008; previously cited), which disclose at least “an electronic substrate processing system comprising a processing equipment for processing electronic substrates including semiconductor wafers and liquid crystal substrates; a cleaning equipment for cleaning said electronic substrate in a predetermined processing step; a portable closed container for accommodating a cassette containing said electronic substrate; a purging station for gas-purging said portable closed containers; and a storage member for storing said portable closed containers, wherein said cassette accommodates said electronic substrates which have been cleaned by said cleaning equipment being set in said portable closed container and purged with an inert gas in said purging station, and said portable closed container or containers is stored in said storing section when necessary” (the abstract), wherein “in FIG. 1, reference numeral 100 designates a film forming deposition system arranged in a clean room; 200, a wafer cleaning device; 300, a purging station; 400, a clean stocker; 500, a transfer robot on which a product placing device is mounted; and 30, a portable closed container” (col. 4, lines 1-5), “in the above-described purging stations, the container storing section 1C is provided for empty containers; however, it may be used for storing the containers gas-purged; that is, it may be used as a container clean stocker” (col. 8, lines 8-14) and “in the above-described embodiment, the purging station is separate from the cleaning equipment” (col. 8, lines 31-33).

13. Claims 2 and 18 are rejected under 35 U.S.C. §103(a), as being unpatentable over Yamashita et al. (U.S. Patent No. 5,746,008), as applied to claims 1 and 16 above, further in view of Wehrung et al. (U.S. Patent Application Publication No. 2002/0164242).

Wehrung et al. disclose a “control system for transferring and buffering material in a material transport system. A transport system and method for moving an article between a conveyor and a workstation. A robot works in conjunction with transportation buffer control system to move Pods between storage shelves, load ports and I/O ports without intervention of the material handling controller. The robots include vertical movement mechanisms and horizontal movement mechanisms together with gripping devices to handle the Pods. Movement of Pods between storage shelves, load ports and I/O ports is seen as a single activity by the material control system” (the abstract), including the well-known use of various transfer carriers and MCS in a semiconductor fabrication facility (see at least, paragraph 0017). It would

have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such transfer carriers in the system of Yamashita et al., because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

14. Claims 3-5, 12-15, 17 and 25-28 are rejected under 35 U.S.C. §103(a), as being unpatentable over Yamashita et al. (U.S. Patent No. 5,746,008), as applied to claims 1, 11 and 16 above, further in view of Pasadyn et al. (U.S. Patent No. 6,678,570).

Pasadyn et al. disclose a “method for determining output characteristics of a workpiece includes generating a tool state trace related to the processing of a workpiece in a tool; comparing the generated tool state trace to a library of reference tool state traces, each reference tool state trace having an output characteristic metric; selecting a reference tool state trace closest to the generated tool state trace; and determining an output characteristic of the workpiece based on the output characteristic metric associated with the selected reference tool state trace. A manufacturing system includes a tool and a tool state monitor. The tool is adapted to process a workpiece. The tool state monitor is adapted to generate a tool state trace related to the processing of a workpiece in the tool, compare the generated tool state trace to a library of reference tool state traces, each reference tool state trace having an output characteristic metric, select a reference tool state trace closest to the generated tool state trace, and determine an output characteristic of the workpiece based on the output characteristic metric associated with the selected reference tool state trace” (the abstract), in a semiconductor fabrication facility, including the well-known use of MES to schedule processes using various identification elements (see at least, col. 1, line 28 - col. 2, line 25; col. 3, line 54 - col. 4, line 34). It would have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such an operating system in the system of Yamashita et al., because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

15. Claims 7, 8, 20 and 22 stand rejected under 35 U.S.C. §103(a), as being unpatentable over Yamashita et al. (U.S. Patent No. 5,746,008), as applied to claims 1 and 16 above, further in view of Chen et al. (U.S. Patent No. 6,821,891).

Chen et al. does not specifically provide for either a stocker or OHB as one of the devices. However, in this regard, Chen et al. does specify that the “processing chamber 200 may be integrated into an integrated processing platform, such as an Endura™ platform also available from Applied Materials, Inc.” (col. 5, lines 11-14), details of which were incorporated by reference. Such a platform inherently utilizes transfer stations,

such as stockers, buffers and FOUPs (see for example, Vepa et al. (U.S. Patent Application Publication No. 2001/0027082 and U.S. Patent No. 6,852,012), Jevtic et al. (U.S. Patent Nos. 6,519,498 and 6,224,638) and Venkatesh et al. (U.S. Patent No. 6,074,443); all previously cited). Therefore, Chen et al. inherently provides for the presence of such transfer stations in a semiconductor manufacturing line. It would have been obvious, to one having ordinary skill in the art, at the time the instant invention was made, to utilize such elements in the system of Yamashita et al., because it has been held to be within the general skill of a worker in the art to select a known material on the basis of its suitability for the intended use as a matter of obvious design choice. *In re Leshin*, 125 USPQ 416.

16. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. §103(a), Examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR §1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for Examiner to consider the applicability of 35 U.S.C. §103(c) and potential 35 U.S.C. §102(e), (f) or (g) prior art under 35 U.S.C. §103(a).

17. The prior art made of record and not relied upon is considered pertinent to Applicant's disclosure. Applicant is advised to carefully review the cited art, as evidence of the state of the art, in preparation for responding to this Office action.

18. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria N. Von Buhr whose telephone number is 571-272-3755. The examiner can normally be reached on M-F (9am-5pm).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Leo Picard can be reached on 571-272-3749. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



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